

Silicon NPN Power Transistors

2SD717

DESCRIPTION

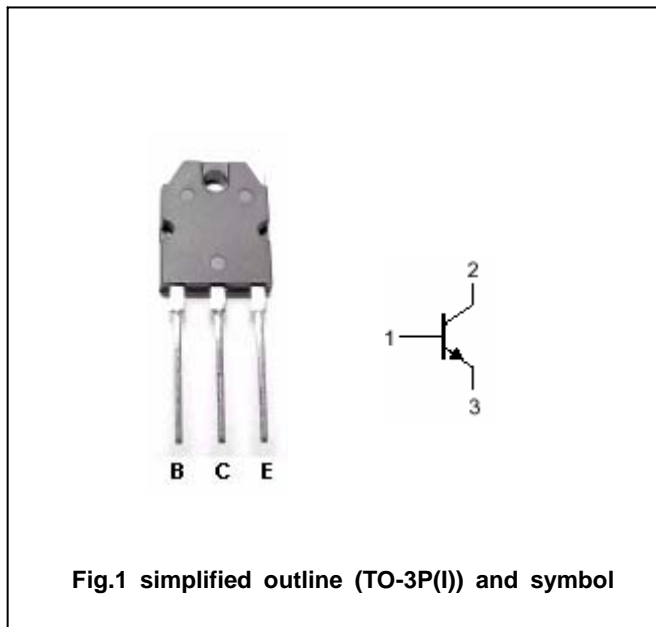
- With TO-3P(I) package
- Low collector saturation voltage
- High collector power dissipation

APPLICATIONS

- High power switching applications
- DC-DC converter and DC-AC inverter applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	70	V
V _{CEO}	Collector-emitter voltage	Open base	50	V
V _{EBO}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		10	A
I _B	Base current		2	A
P _T	Total power dissipation	T _C =25	80	W
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =50mA, I _B =0	50			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =6A; I _B =0.3A			0.4	V
V _{BEsat}	Base-emitter saturation voltage	I _C =6A; I _B =0.3A			1.2	V
I _{CBO}	Collector cut-off current	V _{CB} =70V; I _E =0			10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			10	μA
h _{FE-1}	DC current gain	I _C =1A; V _{CE} =1V	70		240	
h _{FE-2}	DC current gain	I _C =6A; V _{CE} =1V	30			
f _T	Transition frequency	I _C =1A; V _{CE} =4V		10		MHz
C _{ob}	Output capacitance	I _E =0; V _{CB} =10V; f=1MHz		350		pF

Switching times

t _{on}	Turn-on time	I _{B1} =-I _{B2} =0.3A; R _L =5 Ω; V _{CC} =30V		0.3		μs
t _s	Storage time			2.5		μs
t _f	Fall time			0.4		μs

◆ h_{FE-1} Classifications

O	Y
70-140	120-240

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PACKAGE OUTLINE

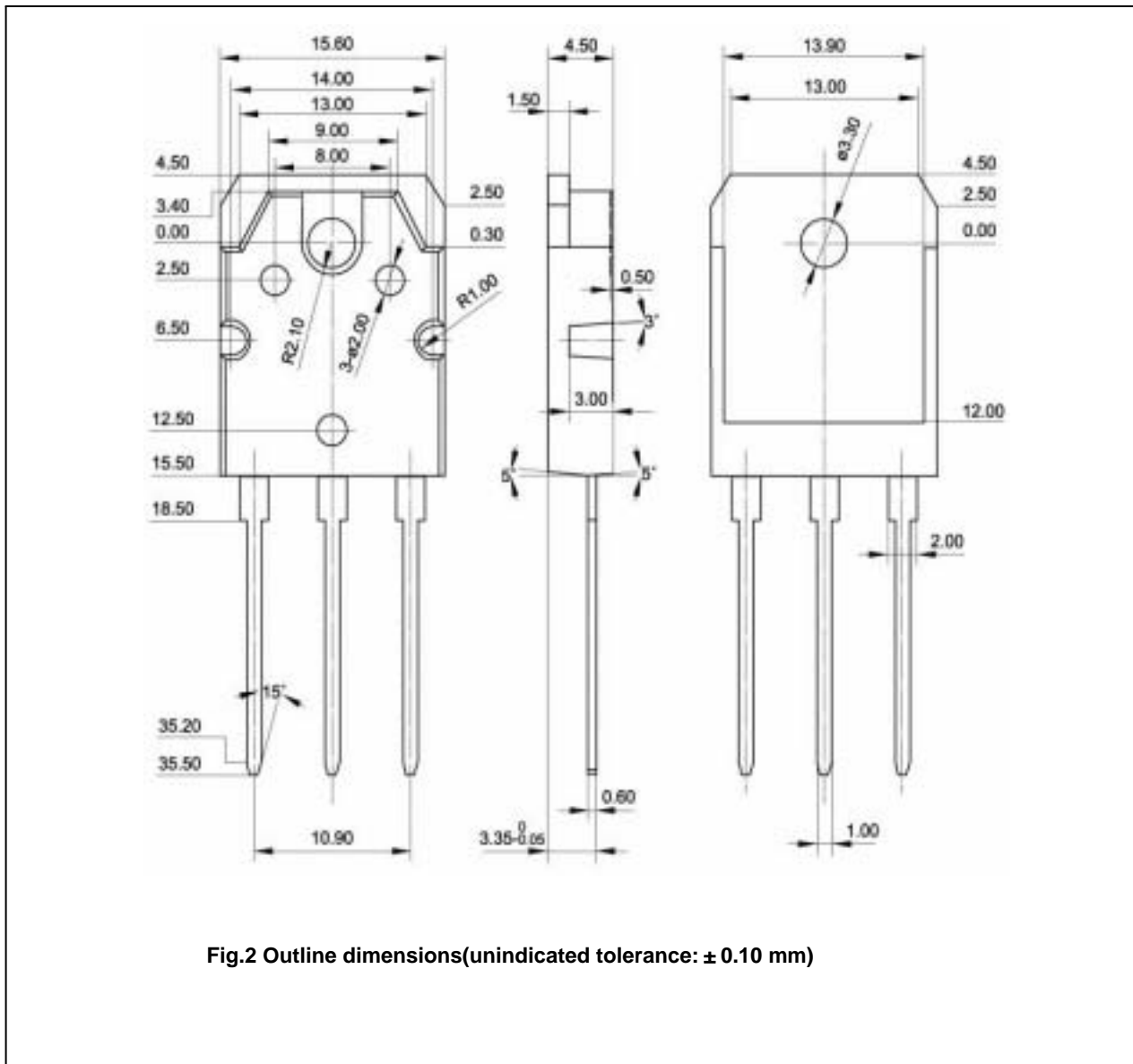


Fig.2 Outline dimensions(unindicated tolerance: ± 0.10 mm)